**Timming Mode**
Application of input voltage to the timer starts the time delay. At the end of the delay period the load is energized. To reset, remove the input voltage to the timer.

**THCRU Timers**
The THCRU timer is a universal voltage, solid state output unit with digital timing in a one-inch diameter round case. THCRU offers wide operating voltages of 19–265VAC and 10–120VDC in a single unit. Additional benefits include small size and easy installation and hook-up.

**FEATURES**
- Accurate digital timing, solid state output
- 1 Amp current load handling, 10 Amps single cycle surge
- Wide universal operating voltages in a single timer: 19–265VAC and 10–120VDC
- Time delay is factory fixed per customer request
- Repeat accuracy is ±0.1%  Set accuracy is ±5%
- Totally encapsulated for protection from harsh environments
- 100% operational testing before shipping
- RoHS compliance available

120514
**SPECIFICATIONS**

- **Universal Input Voltage:** 10–120VDC and 19–265VAC
- **Repeat Accuracy:** ±0.1% under fixed conditions
- **Time Delay:** Factory fixed per customer request (See examples in Options)
- **Reset/Recycle Time:** 100 milliseconds
- **Tolerances:** Fixed at ±10% of nominal time
- **Recycle Effect:** None
- **Temperature Ranges:**
  - **Storage:** -40°C to +85°C
  - **Operating:** -25°C to +65°C
- **Life Expectancy:** No predictable failure if used within operating parameters
- **Protection:**
  - **Transient Protection:** Internal MOV
  - **Dielectric Strength:** 1800V RMS 60Hz
- **Power Requirements:** 0.25 watts max
- **Solid State Output:** SPST (N.O.)
- **Load Current:** 1 amp continuous, 10 amps single-cycle surge
- **Time Cycle Interrupt:** Input voltage break resets timer to original state

**OPTIONS SELECTION**

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<td>THCR</td>
<td>U Universal 19–265VAC and 10–120VDC</td>
<td>001S 1 second, 010SS 10.5 seconds, 1200SS 1200 seconds</td>
<td>C Fixed, internal, factory set</td>
<td>S Round case with wires</td>
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</table>

**EXAMPLE** **THCR U 001S C S** is a THCRU series timer with 1 second fixed time delay.